EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurais	Time Stamp
\$1	1818	(716/1).CCLS.	US-PGPUB; USPAT	OR	OFF	2007/12/20 11:35
S2	1628	(716/2).CCLS.	US-PGPUB; USPAT	OR	OFF	2007/12/20 11:35
S3	3169	S1 S2	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/20 11:20
S4	3	(("20020053039") or ("0943978")).PN.	US-PGPUB; USPAT; EPO; DERWENT	OR	OFF	2007/12/20 11:34
\$5 	1079	(713/323).CCLS.	US-PGPUB; USPAT; EPO; DERWENT	OR	OFF	2007/12/20 11:34
S6	1079	(713/323).CCLS.	US-PGPUB; USPAT	OR	OFF	2007/12/20 12:23
57	595	whitmore-s\$.xp. or whitmore-s\$.xa.	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM TDB	OR	ON	2007/12/20 12:31
S8	0	57 and integrated adj3 cicuit and operat\$4	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/20 12:24
S9	339	57 and integrated adj3 circuit and operat\$4	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/20 12:24
510	390	S7 and integrated adj3 circuit sameoperat\$4	US-PGPUB; i USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/20 12:24

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		1				
S11	142	S7 and integrated adj3 circuit same operat\$4	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/20 12:25
S12	4	S7 and integrated adj3 circuit same operat\$4 same threshold	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/20 12:27
513	0	57 and integrated adj3 circuit same operat\$4 same heat	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/20 12:27
S14	25	S7 and integrated adj3 circuit same operat\$4 same clock	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/20 12:27
S15	23	S14 not S12	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON i	2007/12/20 12:27
S16	27	S7 and operating with integrated adj2 circuit	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/20 12:31
\$17	27	S7 and (operating with integrated adj2 circuit)	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/20 12:31
S18	26	\$17 and "716"/\$.ccls.	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	. ON	. 2007/12/20 12:34

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519 :	79	S7 and 716/2.ccls.	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/20 12:40
S20	2.	57 and (operat\$4 and integrated and circuit).ti.	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/20 12:41
S21	16	(ic or integrated adj2 circuit) and ((voltage near3 island) or (triple near3 well)) and (monitor\$3 with condition) and (supply or threshold or clock or frequency)	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/20 12:46